

EXCALIBUR
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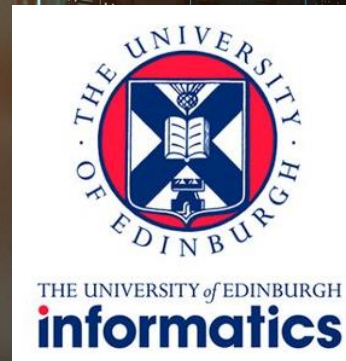
XDSL CROSS CUTTING PROJECT

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Engineer's House, 11-12 October 2023

epcc

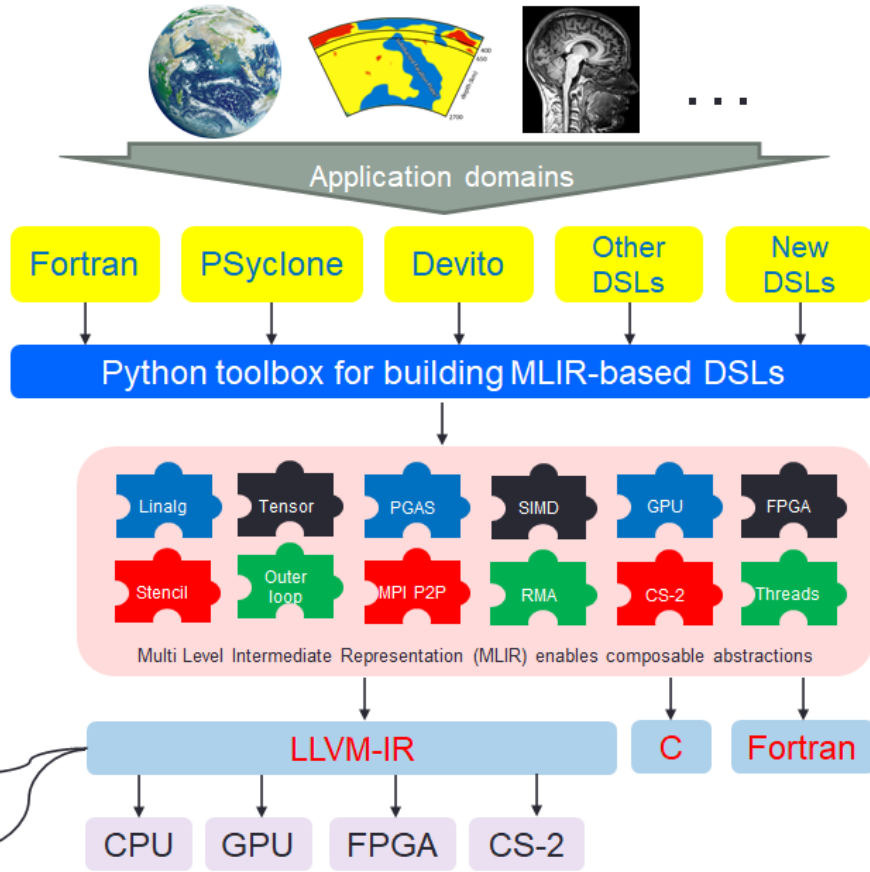
Imperial College
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Project Status

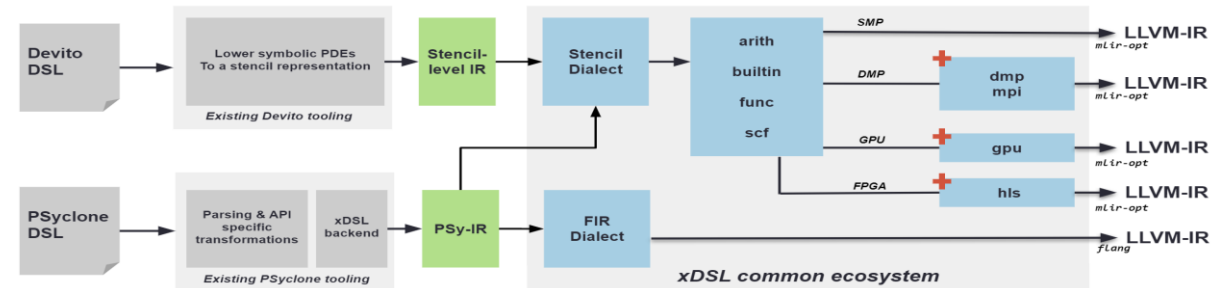


- A common ecosystem for DSLs based upon MLIR and LLVM
 - A burgeoning and growing open source project

1. Very significant development of Python toolbox
 - 117 stars on Github, 40 forks, 43 contributors



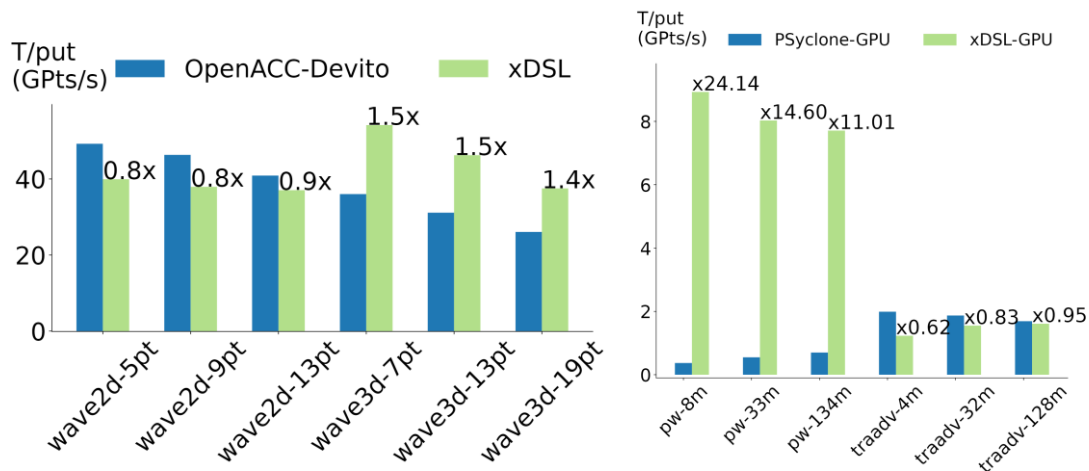
2. Lots of work done on integration with Devito and PSyclone
 - These are a thin abstraction layer atop a common compiler ecosystem, as we had envisaged



3. Expanded our efforts to integrate with the Flang Fortran compiler
 - Combining a general purpose and domain-specific compiler results in between a 3 to 5 times increase in performance for applicable benchmarks, and auto-parallelisation/auto-porting

Current Challenges / Activities

- We are currently working heavily on performance tuning
 - Across the entire xDSL/MLIR technology stack
 - Have been heavily stencil based, will expand to other computational patterns (and benchmarks)



Looking to merge MPI/distributed memory dialects into MLIR



MLIR

- These are crucial in xDSL to support the HPC DSLs, and this demonstrates xDSL can be used for rapid prototyping
- Presentation later in the year with MLIR community to drive this forwards

- Novel HPC architectures
 - Between 80 and 100 times faster than state of the art (DaCe) for auto-optimised FPGA code execution
 - Currently exploring AMD Xilinx AI engines and Cerebras CS-2 (with CGRA H&ES testbed)

- Our experiments with domain specific abstractions in the Flang compiler have been very successful
 - Plenty of opportunity to benefit the Flang compiler, which is strategically important for HPC
 - How is best to do this in a structured way?

Open questions/areas to connect on

1. Potential users of xDSL
 - Do you have a DSL that would benefit from our ecosystem?
 - We have some further benchmarks in mind, but additional ones would be interesting
2. How best can we ensure the longevity of this open source project after the ExCALIBUR project ends?
 - There is massive potential here, how can we best leverage this into the future?
 - Ideas around activities to ensure and encourage long term support would be appreciated



xDSL



Here in Bristol...

xDSL

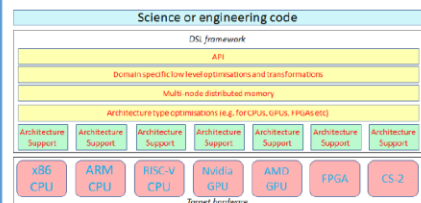
A common compiler ecosystem for domain specific languages

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What are Domain Specific Languages (DSLs)?

There is an explosion of hardware architectures for HPC, and as we move into the exascale era a key challenge is how to fully exploit such complex, highly parallel heterogeneous supercomputers.



DSLs provide a separation of concerns and are arguably the only way in which we can effectively program and exploit future exascale supercomputers

Why are DSLs not ubiquitous in HPC?

A major disadvantage is in the siloing of compiler infrastructure, where DSLs tend to share very little or no infrastructure between them at the toolchain level.

This siloing of toolchains results in:

- ✗ Uncertainty around long term maintenance
- ✗ Reinventing the wheel
- ✗ Considerable effort needed to develop a DSL
- ✗ Limited opportunities for DSLs to target multiple domains
- ✗ Limited third-party tools
- ✗ Considerable effort for new architectures

We must solve the challenges around siloing in order for DSLs to become more widespread

Hence our vision: A DSL being a thin layer atop an existing, mature ecosystem with a wealth of third party tools

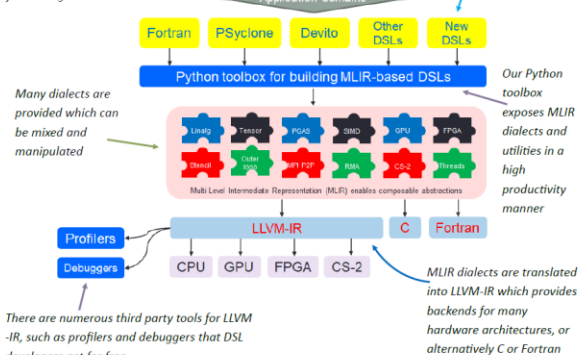
30 second summary

- Domain Specific Languages (DSLs) have great potential.
- Underlying toolchains are often siloed and share very little or no infrastructure.
- We are developing a Python ecosystem so DSL developers can write a thin-layer on-top of the existing, well supported, MLIR/LLVM compiler infrastructure.

What is our xDSL ecosystem?

A Python toolbox based upon MLIR, integration with MLIR & series of HPC dialects

Many application domains can benefit from using DSLs on HPC. Variety of DSLs can build upon our technology.

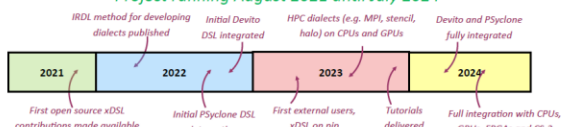


There are numerous third party tools for LLVM -IR, such as profilers and debuggers that DSL developers get for free

Our ecosystem provides:

- ✓ **Composability** where DSL owners choose what parts of our ecosystem to leverage
- ✓ **Interoperability** between DSLs
- ✓ **Code reuse** of toolchain infrastructure
- ✓ **Longevity** of DSL compiler technology as we build upon LLVM and MLIR
- ✓ **Performance** because dialects and backends are developed by experts
- ✓ **Productivity** for both the application and DSL developers due to code reuse
- ✓ **Portability** for application codes and DSLs across architectures.

Project running August 2021 until July 2024



Building on LLVM and MLIR

LLVM is a collection of common compilation tools and infrastructure. There are numerous LLVM backends available for different hardware, targeted via LLVM-IR.

Get Involved

xDSL is open source and a burgeoning community
www.xdsl.dev
github.com/xdslproject
[xdsl.zulipchat.com](https://www.xdsl.dev/discord)



MLIR enables representing and mixing dialects of intermediate representations and abstractions, thus providing easier integration, reuse, and optimisation. However, currently written in C++ there is a fairly steep learning curve that our Python toolbox looks to address.

Evaluating the xDSL framework

We have selected two existing DSLs to act as evaluators



Devito is a Python based DSL for symbolic finite difference computation. Especially popular for seismicological simulations, there are a variety of users from oil & gas exploration to glaciologists

PSyclone is a Fortran based DSL for developing weather and climate codes. It is used extensively by the Met Office



Above illustrates translating the PSyclone DSL's Fortran code into LLVM-IR using our approach

Funded by ExCALIBUR

The UK ExCALIBUR program address the challenges and opportunities offered by computing at the exascale and aims to deliver the next generation of HPC simulation software and tooling
<https://excalibur.ac.uk/>



George Bisbas



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Nick Brown



xDSL

